

INFORMATION DISCLOSURE CITATION*(USE SEVERAL SHEETS IF NECESSARY)*

ATTY DOCKET NO.

POU920020103US1

SERIAL NO.

APPLICANT(S)

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,220,654	06/15/93	Benson et al.	395	275	
	AB	5,423,026	06/06/95	Cook et al.	395	575	
	AC	5,465,355	11/07/95	Cook et al.	395	200.15	
	AD	5,548,791	08/20/96	Casper et al.	395	858	
	AE	5,568,648	10/22/96	Coscarella et al.	395	862	
	AF	5,644,712	07/01/97	Coscarella et al.	395	200.01	
	AG	5,664,219	09/02/97	Glassen et al.	395	825	
	AH	5,671,441	09/23/97	Glassen et al.	395	828	
	AI	6,266,325 B1	07/24/01	Ishioka et al.	370	248	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION		
							NO	YES	ABSTRACT
	AJ	EP096937A1	01/05/00	EPO	G06F	11/00			
	AK	JP11136254	05/21/99	Japan	H04L	12/28	X		Yes

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
	AL	"Technique For Resetting A Single Channel Path Under Operator Control In IBM System/370XA," IBM Technical Disclosure Bulletin, Vol. 26, No. 10B March 1984, p. 5516
	AM	"Multiple-Processor Origin Mapping In Buffered Subsystem," IBM Technical Disclosure Bulletin, Vol. 25, No. 5, October 1982, pp. 2492-2494
	AN	"Multiple Logical Input/Output Subsystem Facility," Brice et al., U.S. Serial No. 10/436,021, filed May 12, 2003
	AO	"Method, System And Program Products For Identifying Communications Adapters Of A Computing Environment," Brice et al., U.S. Serial No. 10/436,385, filed May 12, 2003
	AP	"zSeries 990 CHPID Mapping Tool User's Guide," IBM Publication No. G28-6825-00 (June 2003)
	AQ	"IBM CHPID Mapping Tool – Standalone Version," IBM Corporation, September 2002

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